

04-23-01

PATENT

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Case Docket No. ADTST.021AUS

Date: April 21, 2001

Page 1

Jc984 U.S. PTO
04/21/01ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

ATTENTION: APPLICATION BRANCH

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): **Leon Lee Chen et al.**For: **SEMICONDUCTOR TEST SYSTEM WITH TIME CRITICAL SEQUENCE GENERATION USING
GENERAL PURPOSE OPERATING SYSTEM**

Enclosed are:

- (X) 7 sheet(s) of drawings.
- (X) 19 pages of specification, claims and abstract.
- (X) **UNSIGNED** declaration and power of attorney.
- (X) Return prepaid postcard.

J1036 U.S. PTO
09/839013
04/21/01

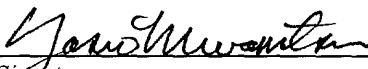
CLAIMS AS FILED

FOR	NUMBER FILED	NUMBER EXTRA	RATE	FEE
Basic Fee			\$710	\$710
Total Claims	12 - 20 =	0 x	\$18	\$0
Independent Claims	2 - 3 =	0 x	\$80	\$0
If application contains any multiple dependent claims(s), then add			\$270	\$0
TOTAL FILING FEE				\$710

(X) A check in the amount of \$710 to cover the filing fee is enclosed.

SEND ALL CORRESPONDENCE TO:

MURAMATSU & ASSOCIATES
7700 Irvine Center Drive
Suite 225
Irvine, CA 92618
Tel: (949) 753-1127


Signature
Yasuo Muramatsu
Printed Name
38,684
Registration Number

09839013-042101

law offices of
Muramatsu & Associates
7700 Irvine Center Drive, Suite 225
Irvine, CA 92618, U.S.A.

Tel (949) 753-1127
Fax (949) 753-1071
E-mail: muramat@ix.netcom.com

April 21, 2001

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Washington, D.C. 20231

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Attorney Docket No. : ADTST.021AUS

Applicant(s) : Leon Lee Chen et al.

For : SEMICONDUCTOR TEST SYSTEM WITH
TIME CRITICAL SEQUENCE
GENERATION USING GENERAL
PURPOSE OPERATING SYSTEM

Attorney : Yasuo Muramatsu

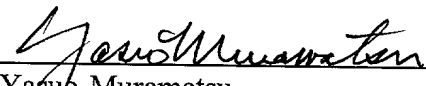
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I hereby certify that the accompanying

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are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and are addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.



Yasuo Muramatsu